UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO. : 6,798,705 B2
DATED : September 28, 2004
INVENTOR(S) : R. Jacob Baker

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title page,
Item [56], **References Cited**, U.S. PATENT DOCUMENTS, reads

"3,818,501 A *	6/1974	Florino	360/40" should read
-- 3,818,501 A *	6/1974	Fiorino	360/40 --.

Column 1,
Line 43, reads "at relatively cheap." should read -- relatively cheaply. --.

Column 3,
Line 3, reads "a integrator ouput" should read -- an integrator output --.
Line 15, reads "of clocked comparator" should read -- of a clocked comparator --.

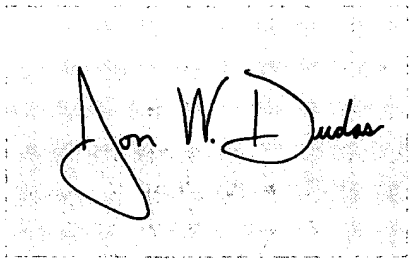
Column 6,
Line 53, reads "the coupling off" should read -- the coupling of --.

Column 8,
Line 9, reads "based on logic state" should read -- based on the logic state --.

Column 10,
Line 32, reads "a dare stare of a memory cell," should read -- a data state of a memory cell, --.
Line 63, "value or the clocked output signal." should read -- value of the clocked output signal. --.

Signed and Sealed this

Eighth Day of November, 2005



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